

# 50 kPa On-Chip Temperature Compensated and Calibrated Silicon Pressure Sensors

The MPXM2051G device is a silicon piezoresistive pressure sensor providing a highly accurate and linear voltage output - directly proportional to the applied pressure. The sensor is a single, monolithic silicon diaphragm with the strain gauge and a thin-film resistor network integrated on-chip. The chip is laser trimmed for precise span and offset calibration and temperature compensation.

## Features

- Temperature Compensated Over 0°C to +85°C
- Available in Easy-to-Use Tape & Reel
- Ratiometric to Supply Voltage
- Gauge Ported

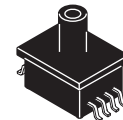
## Typical Applications

- Pump/Motor Controllers
- Robotics
- Level Indicators
- Medical Diagnostics
- Pressure Switching
- Non-Invasive Blood Pressure Measurement

## MPXM2051G SERIES

**COMPENSATED AND CALIBRATED PRESSURE SENSOR**  
 0 TO 50 kPa (0 TO 7.25 psi)  
 40 mV FULL SCALE SPAN  
 (TYPICAL)

## MPAK PACKAGES



**MPXM2051GS/GST1**  
**CASE 1320A-02**

ORDERING INFORMATION					
Device Type	Options	Case No.	MPX Series Order No.	Packing Options	Device Marking
Ported	Absolute, Axial Port	1320A	MPXM2051GS	Rails	MPXM2051G
	Absolute, Element Only	1320A	MPXM2051GST1	Tape & Reel	MPXM2051G

Pin Number			
1	Gnd	3	V <sub>S</sub>
2	+V <sub>out</sub>	4	-V <sub>out</sub>

Figure 1 shows a block diagram of the internal circuitry on the stand-alone pressure sensor chip.

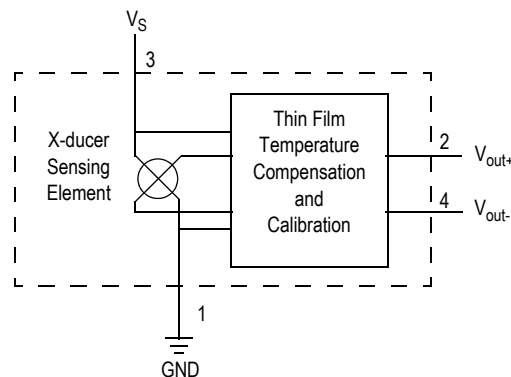


Figure 1. Temperature Compensated Pressure Sensor Schematic

## VOLTAGE OUTPUT VERSUS APPLIED DIFFERENTIAL PRESSURE

The differential voltage output of the sensor is directly proportional to the differential pressure applied.

The output voltage of the differential or gauge sensor increases with increasing pressure applied to the pressure side relative to the vacuum side. Similarly, output voltage increases as increasing vacuum is applied to the vacuum side relative to the pressure side.

**Table 1. Maximum Ratings<sup>(1)</sup>**

Rating	Symbol	Value	Unit
Maximum Pressure	$P_{max}$	200	kPa
Storage Temperature	$T_{stg}$	-40 to +125	°C
Operating Temperature	$T_A$	-40 to +125	°C

1. Exposure beyond the specified limits may cause permanent damage or degradation to the device.

**Table 2. Operating Characteristics** ( $V_S = 10$  Vdc,  $T_A = 25^\circ\text{C}$ .)

Characteristic	Symbol	Min	Typ	Max	Unit
Pressure Range <sup>(1)</sup>	$P_{OP}$	0	—	50	kPa
Supply Voltage <sup>(2)</sup>	$V_S$	—	10	16	Vdc
Supply Current	$I_o$	—	6.0	—	mAdc
Full Scale Span <sup>(3)</sup>	$V_{FSS}$	38.5	40	41.5	mV
Offset <sup>(4)</sup>	$V_{off}$	-1.0	—	1.0	mV
Sensitivity	$\Delta V/\Delta P$	—	0.8	—	mV/kPa
Linearity <sup>(5)</sup>	—	-0.3	—	0.3	% $V_{FSS}$
Pressure Hysteresis <sup>(5)</sup> (0 to 50 kPa)	—	—	$\pm 0.1$	—	% $V_{FSS}$
Temperature Hysteresis <sup>(5)</sup> (-40°C to +125°C)	—	—	$\pm 0.5$	—	% $V_{FSS}$
Temperature Effect on Full Scale Span <sup>(5)</sup>	$TCV_{FSS}$	-1.0	—	1.0	% $V_{FSS}$
Temperature Effect on Offset <sup>(5)</sup>	$TCV_{off}$	-1.0	—	1.0	mV
Input Impedance	$Z_{in}$	1000	—	2500	$\Omega$
Output Impedance	$Z_{out}$	1400	—	3000	$\Omega$
Response Time <sup>(6)</sup> (10% to 90%)	$t_R$	—	1.0	—	ms
Warm-Up	—	—	20	—	ms
Offset Stability <sup>(7)</sup>	—	—	$\pm 0.5$	—	% $V_{FSS}$

- 1.0 kPa (kiloPascal) equals 0.145 psi.
- Device is ratiometric within this specified excitation range. Operating the device above the specified excitation range may induce additional error due to device self-heating.
- Full Scale Span ( $V_{FSS}$ ) is defined as the algebraic difference between the output voltage at full rated pressure and the output voltage at the minimum rated pressure.
- Offset ( $V_{off}$ ) is defined as the output voltage at the minimum rated pressure.
- Accuracy (error budget) consists of the following:
  - Linearity: Output deviation from a straight line relationship with pressure, using end point method, over the specified pressure range.
  - Temperature Hysteresis: Output deviation at any temperature within the operating temperature range, after the temperature is cycled to and from the minimum or maximum operating temperature points, with zero differential pressure applied.
  - Pressure Hysteresis: Output deviation at any pressure within the specified range, when this pressure is cycled to and from the minimum or maximum rated pressure, at 25°C.
  - TcSpan: Output deviation at full rated pressure over the temperature range of 0 to 85°C, relative to 25°C.
  - TcOffset: Output deviation with minimum rated pressure applied, over the temperature range of 0 to 85°C, relative to 25°C.
- Response Time is defined as the time for the incremental change in the output to go from 10% to 90% of its final value when subjected to a specified step change in pressure.
- Offset stability is the product's output deviation when subjected to 1000 hours of Pulsed Pressure, Temperature Cycling with Bias Test.

## LINEARITY

Linearity refers to how well a transducer's output follows the equation:  $V_{out} = V_{off} + \text{sensitivity} \times P$  over the operating pressure range. There are two basic methods for calculating nonlinearity: (1) end point straight line fit (see Figure 2) or (2) a least squares best line fit. While a least squares fit gives

the “best case” linearity error (lower numerical value), the calculations required are burdensome.

Conversely, an end point fit will give the “worst case” error (often more desirable in error budget calculations) and the calculations are more straightforward for the user. The specified pressure sensor linearities are based on the end point straight line method measured at the midrange pressure.

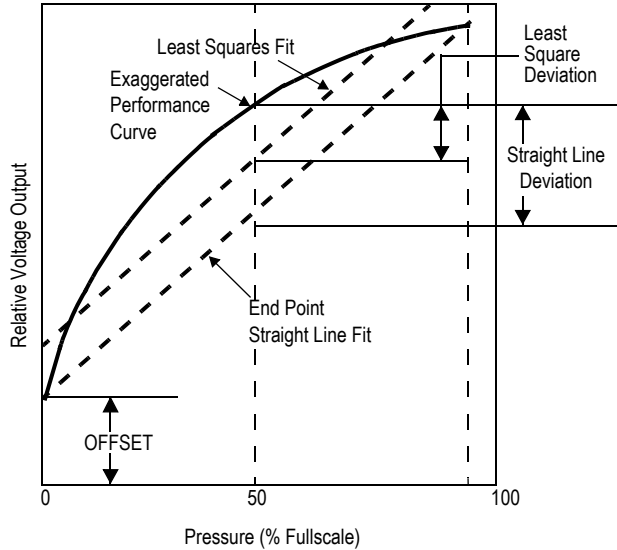


Figure 2. Linearity Specification Comparison

## ON-CHIP TEMPERATURE COMPENSATION AND CALIBRATION

Figure 3 shows the minimum, maximum and typical output characteristics of the MPXM2051G series at 25°C. The

output is directly proportional to the differential pressure and is essentially a straight line.

A silicone gel isolates the die surface and wire bonds from the environment, while allowing the pressure signal to be transmitted to the silicon diaphragm.

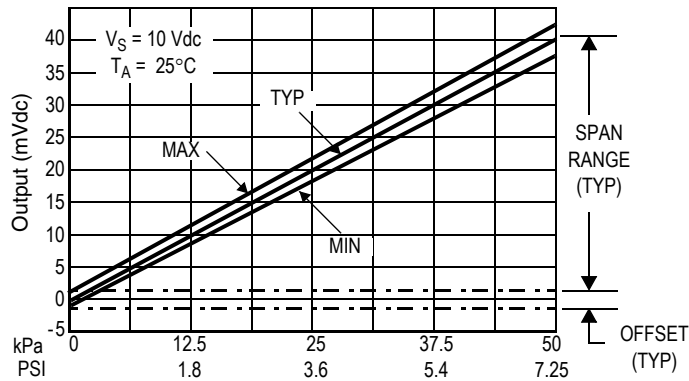
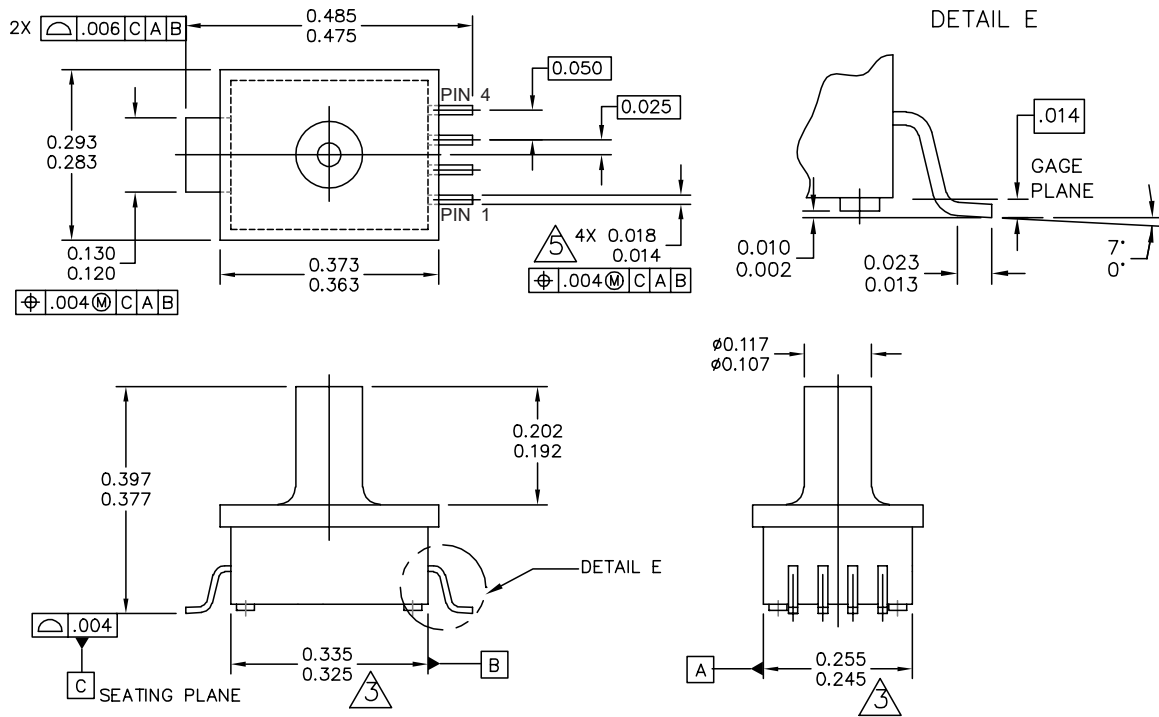


Figure 3. Output versus Pressure Differential

# PACKAGE DIMENSIONS



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**CASE 1320A-02  
ISSUE A**

## PACKAGE DIMENSIONS

### NOTES:

1. DIMENSIONS ARE IN INCHES.

2. INTERPRET DIMENSIONS AND TOLERANCES PER ASME Y14.5M-1994.

3. DIMENSIONS DOES NOT INCLUDE MOLD FLASH OR PROTRUSION. MOLD FLASH OR PROTRUSION SHALL NOT EXCEED .006" PER SIDE.

4. ALL VERTICAL SURFACES TO BE 5" MAXIMUM.

5. DIMENSION DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE .008 MAXIMUM.

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**MPXM2051G**

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